

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

DALSON YE SENG KIM
JEFFREY TOH TUCK FOOK
LEE CHOON KUAN

Serial No. 10/633,434

Art Unit: 2823

Filing Date: 08/01/2003

Examiner: Lee, Hsien Ming

For: Semiconductor Component Having
Chip On Board Leadframe
(As Amended)

Attorney Docket No. 02-1467

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56
August 19, 2004

Mail Stop AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

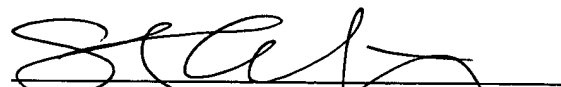
The citations listed on the accompanying Form PTO/SB/o8, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:

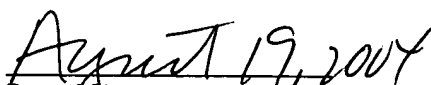
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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Mail Stop AMENDMENT, Commissioner for Patents, PO BOX 1450, Alexandria, VA 22313-1450 on this 19th day of August, 2004.


Date of Signature


Stephen A. Gratton, Attorney for Applicants

